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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
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09/313,225 05/17/99 MCELVAIN

K 02986 P006

EXAMINER

MM71/0628

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ART UNIT INCLUDING PAPER NUMBER

DATE MAILED: 06/28/01

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Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Office Action Summary

Application No.

09/313,225

Applicant(s)

MC ELVAIN ET AL.

Examiner

Gene N Auduong

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) ____ is/are pending in the application.
- 4a) Of the above claim(s) 1-73 is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-73 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claims ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are objected to by the Examiner.
- 11) ☐ The proposed drawing correction filed on ____ is: a) ☐ approved b) ☐ disapproved.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. ____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

- 15) ☒ Notice of References Cited (PTO-892)
- 16) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 17) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) ____.
- 18) ☐ Interview Summary (PTO-413) Paper No(s). ____.
- 19) ☐ Notice of Informal Patent Application (PTO-152)
- 20) ☐ Other: _____.

DETAILED ACTION

Drawings

The drawings are objected to because of the problem addressed in the attached PTO-948. Correction is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

Claims 1-73 are rejected under 35 U.S.C. 102(e) as being anticipated by Eng (U.S. Pat. No. 6,145,117).

Eng discloses a RTL optimization system and RTL design methodology for designing an integrated circuit (IC) comprising: a display device; a memory; a processor coupled to the memory and to the display device, the processor allocating a specific portion of a technology independent RTL (register transfer level) netlist to a portion of the IC, the technology independent RTL netlist being stored in the memory, wherein the processor compiles a hardware description language (HDL) code to produce the technology independent RTL netlist and wherein the allocating restricts circuitry created from the specific portion to the portion of the IC; wherein the IC comprises one of a programmable logic device or an ASIC or wherein the processor maps the technology independent RTL netlist to a selected technology architecture, wherein the processor maps the technology independent RTL netlist after the processor performs

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the allocating, wherein the processor performs a place and route operation after the processor maps the technology independent RTL netlist, wherein the place and route operation creates a representation of circuitry in the selected technology architecture, wherein the processor optimizes a design of the IC after the processor performs the allocating, wherein the processor maps the technology independent RTL netlist after the processor performs the allocating or wherein the processor maps portions of the technology independent RTL netlist to the selected technology architecture to generate estimates of IC resources, wherein the processor maps the portions after the processor compiles the HDL code, wherein the processor displays the estimates on the display device and stores the estimates in the memory, wherein the processor displays graphical representations of the area of the IC on the display device and displays on the display device representations of portions of the technology independent RTL netlist and wherein the processor performs the allocating in response to a command from a user, wherein estimates of area requirements of the portions of the technology independent RTL netlist are displayed on the display device (col. 3, lines 50+; col. 8, lines 20+).

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Pffuger et al. (U.S. Pat. No. 6,192,504)

Dupenloup (U.S. Pat. No. 6,205,572)

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Gene N Auduong whose telephone number is (703) 305-1343.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (703) 308-4910. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.



GA
June 25, 2001



David Nelms
Supervisory Patent Examiner
Technology Center 2800